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SERIAL NUMBER 10/729,831	FILING DATE 12/05/2003 RULE	CLASS 365	GROUP ART UNIT 2827	ATTORNEY DOCKET NO.. 023-0028
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** CONTINUING DATA *****

This application is a CIP of 10/335,078 12/31/2002

** FOREIGN APPLICATIONS *****

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 03/09/2004

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY CA	SHEETS DRAWING 17	TOTAL CLAIMS 44	INDEPENDENT CLAIMS 4
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged	Examiner's Signature	Initials		

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TITLE

NAND memory array incorporating capacitance boosting of channel regions in unselected memory cells and method for operation of same

☐ All Fees☐ 1.16 Fees (Filing)